Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A fuse circuit used for adjusting an analog value based on a setting state of a fuse element, comprising:

a latch circuit which stores a setting state of a fuse element, ~~; and~~

a latch clock generation circuit which generates a latch clock based on a cyclic signal, the latch clock being used for fetching the setting state of the fuse element into the latch circuit,

a test signal holding circuit which holds a test signal for testing the setting state of the fuse element;

a selector which selectively outputs one of the test signal held in the test signal holding circuit and the setting state of the fuse element fetched by the latch circuit based on a select signal; and

a select signal generation circuit which generates the select signal based on a test mode setting signal and the latch clock,

wherein the latch circuit cyclically fetches the setting state of the fuse element based on the latch clock, and

wherein the select signal generation circuit generates the select signal so that the selector selectively outputs the setting state of the fuse element fetched by the latch circuit when the latch clock is input, and

wherein the analog value is adjusted by the latch circuit based on the setting state of the fuse element fetched by the latch circuit.

2-5. (Cancelled).

6. (Currently Amended) A ~~The~~ fuse circuit used for adjusting an analog value based on a setting state of a fuse element, comprising: ~~as defined in claim 1,~~

a latch circuit which stores a setting state of a fuse element; and

a latch clock generation circuit which generates a latch clock based on a cyclic signal, the latch clock being used for fetching the setting state of the fuse element into the latch circuit,

wherein the latch circuit cyclically fetches the setting state of the fuse element based on the latch clock, and

wherein the cyclic signal is one of a frame signal FR, a start pulse signal YD, and a latch pulse signal LP, which specifies one vertical scanning period or one horizontal scanning period and an alternating signal which is used for inverting a voltage applied to a liquid crystal ~~changes for each frame~~.

7-15. (Cancelled).

16. (Original) The fuse circuit as defined in claim 6, further comprising:

a test signal holding circuit which holds a test signal for testing the setting state of the fuse element;

a selector which selectively outputs one of the test signal held in the test signal holding circuit and the setting state of the fuse element fetched by the latch circuit based on a select signal; and

a select signal generation circuit which generates the select signal based on a test mode setting signal and the latch clock,

wherein the select signal generation circuit generates the select signal so that the selector selectively outputs the setting state of the fuse element fetched by the latch circuit when the latch clock is input.

17-20. (Cancelled).

21. (Currently Amended) A display driver circuit comprising:

a fuse circuit which is used for adjusting an analog value based on a setting state of a fuse element; and

~~the fuse circuit as defined in claim 1; and~~

a driver circuit which drives a display panel based on a voltage value or a current value adjusted based on the setting state ~~by the fuse circuit~~ and based on the cyclic signal,

wherein the fuse circuit includes:

a latch circuit which stores the setting state of the fuse element; and

a latch clock generation circuit which generates a latch clock based on a cyclic signal, the latch clock being used for fetching the setting state of the fuse element into the latch circuit,

wherein the latch circuit cyclically fetches the setting state of the fuse element based on the latch clock.

22. (Cancelled).

23. (Currently Amended) A display driver circuit comprising:

a fuse circuit which is used for adjusting an analog value based on a setting state of a fuse element; and

~~the fuse circuit as defined in claim 11; and~~

a driver circuit which drives a display panel based on a voltage value or a current value adjusted based on the setting state ~~by the fuse circuit~~ and based on the cyclic signal,

wherein the fuse circuit includes:

a latch circuit which stores the setting state of the fuse element,

a latch clock generation circuit which generates a latch clock based on a cyclic signal, the latch clock being used for fetching the setting state of the fuse element into the latch circuit,

a test signal holding circuit which holds a test signal for testing the setting state of the fuse element,

a selector which selectively outputs one of the test signal held in the test signal holding circuit and the setting state of the fuse element fetched by the latch circuit based on a select signal; and

a select signal generation circuit which generates the select signal based on a test mode setting signal and the latch clock,

wherein the latch circuit cyclically fetches the setting state of the fuse element based on the latch clock, and

wherein the select signal generation circuit generates the select signal so that the selector selectively outputs the setting state of the fuse element fetched by the latch circuit when the latch clock is input.

24. (Cancelled).

25. (Original) The fuse circuit as defined in claim 1,

wherein the analog value is a current value, voltage value, or oscillation frequency.

26. (Cancelled).

27. (Original) The display driver circuit as defined in claim 21,

wherein the analog value is a current value in the display driver circuit, voltage value in the display driver circuit, or oscillation frequency in the display driver circuit.

28. (Cancelled).

29. (Original) The display driver circuit as defined in claim 23, wherein the analog value is a current value in the display driver circuit, voltage value in the display driver circuit, or oscillation frequency in the display driver circuit.

30. (Cancelled).